

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**10/507117**

Applicant: Hiroyuki TAKAHASHI, et al.

Title: SEMICONDUCTOR MEMORY DEVICE AND SEMICONDUCTOR
MEMORY DEVICE CONTROL METHOD

Appl. No.: Unassigned

Filing Date: 09/10/2004

Examiner: Unassigned

Art Unit: Unassigned

PRELIMINARY AMENDMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Prior to examination of the present Application, Applicants respectfully request that the application be amended as follows:

Amendments to the Specification begin on page 2 of this document.

Remarks/Arguments begin on page 3 of this document.

Please amend the application as follows: